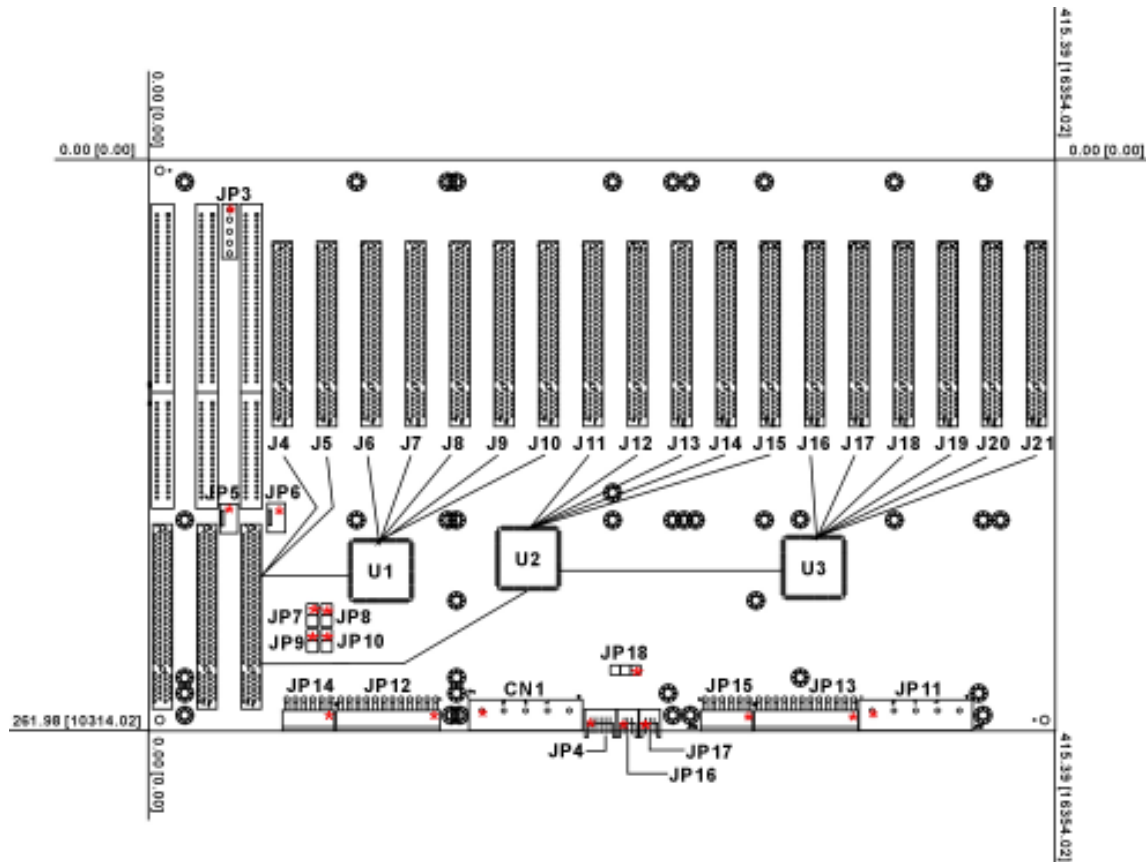


# NBP2018P (20-slot backplane) User's Manual

## Board Dimension and Connector Location



*Note: J4, J5, U1, U2 are driven directly by CPU board, U3 is driven by U2  
 J6 ~ J10 are supported by U1  
 J11 ~ J15 are supported by U2  
 J16 ~ J21 are supported by U3*

## PCI Routing Table

PCI Slot	IDSEL	INT	PCI Slot	IDSEL	INT
J4	P_AD31	B	J15	U2_AD27	D
J5	P_AD30	C	J16	U3_AD31	D
J6	U1_AD25	A	J17	U3_AD30	C
J7	U1_AD31	C	J18	U3_AD29	B
J8	U1_AD30	B	J19	U3_AD28	A
J9	U1_AD29	A	J20	U3_AD27	D
J10	U1_AD28	D	J21	U3_AD26	C
J11	U2_AD31	D	U1	P_AD29	-
J12	U2_AD30	C	U2	P_AD28	-
J13	U2_AD29	B	U3	P_AD24	-
J14	U2_AD28	A			

## Connectors

Connector	Function
JP3, JP4	K/B Connector
JP16	Weak Up Connector
JP5, JP6	Fan Connector
JP12, JP14 / JP13, JP15	Power Connector
JP7, JP8, JP9, JP10	Bridge Voltage Selector
CN1, JP11	Terminal BLock Connector

## Pin Definition

JP3, JP4	
Pin	Name
1	KBCLK
2	KBDATA
3	NC
4	GND
5	+5V

JP5, JP6	
Pin	Name
1	GND
2	+12V
3	NC

JP7, JP8, JP9, JP10	
Pin	Name
1	5V
2	12V
3	-5V
4	-12V
5	3.3V
6	5VSD
7	GND

JP11	
PIN	Block
JP8, JP10	PVIO
JP8, JP10	SVIO

CN1	
PIN	Block
JP7, JP9	PVIO
JP7, JP9	SVIO

JP12, JP13	
Pin	Name
1	NC
2	+5V
3	+12V
4	-12V
5	GND
6	GND
7	GND
8	GND
9	-5V
10	+5V
11	+5V
12	+5V

JP14, JP15	
Pin	Name
1	+5VSB
2	Power On
3	GND
4	GND
5	+3.3V
6	+3.3V

JP16	
Pin	Name
1	+5VSB
2	GND
3	Wake Up

## Power Type Select

### AT - AT Mode

JP17 - N/A

JP18 - N/A

### ATX - AT Mode

	JP17	JP18
Unlock	ATX1 - Switch	1-2
Lock	ATX2 - Switch	2-3

### ATX - ATX Mode

JP16 - Wake Up connector. Use 3-wire cable to connect to CPU board.

JP17 - N/A

JP18 - N/A