PIO-821L/PIO-821H

PCI Bus, 16-ch 12-bit, 45 kS/s Multi-function Board











Features >>>

- PCI Bus (5 V) interface
- 12-bit, 45 kS/s A/D converter
- 16 S.E./8 Diff. analog inputs
- A/D trigger: software trigger, pacer trigger, external trigger
- 16-ch 5 V TTL D/I
- 16-ch 5 V TTL D/O
- Interrupt handling

Introduction

The PIO-821L/H is a high performance multifunction board for PC/AT compatible computers. The PIO-821L provides for low gain (1, 2, 4, 8); and the PIO-821H supports high gain (1, 10, 100, 1000). The PIO-821L/H contains a 12-bit ADC with up to 16 single-ended or 8 differential analog input channels. It also has a 12-bit DAC voltage output and 16 TTL-compatible digital input and digital output channels, respectively. The maximum sampling rate of the A/D converter reaches up to about 45K samples/sec.

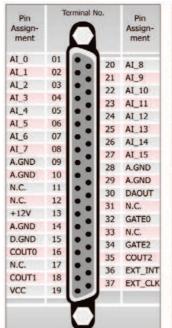
Software -

- DOS Lib and TC sample program (with source codes)
- VB/VC/Delphi/BCB/VB.NET/C#.NET/VC.NET/MATLAB sample programs with source codes
- DLL and OCX SDK for 32-bit/64-bit Windows XP/2003/Vista/2008/7/8
- Support LabVIEW and Linux

Hardware Specifications –

Models	PIO-821L	PIO-821H		
Analog Input				
Channels	12-bit, 16 S.E/8 Diff.			
Accuracy	0.01% of FSR ± 1 LSB @ 25 °C, ± 10 V			
FIFO Size	-			
Sampling Rate	45 kS/s			
Analog Output				
Channels	12-bit, 1-ch			
Accuracy	0.01% of FSR ± 1/2 LSB @ 25 °C, ± 10 V			
Output Range	Unipolar: 0~5V, 0~10 V, 0~Ext Ref			
Output Driving	± 5 mA			
Digital I/O				
Channels	D/I: 16-ch, 5 V/TTL D/O: 16-ch, 5 V/TTL			
Input Voltage	Logic 0: 0.8 V max., Logic 1: 2.0 V min.			
Output Voltage	Logic 0: 0.4 V max., Logic 1: 2.4 V min.			
Output Capability	Sink: 2.4 mA @ 0.8 V, Source: 0.8 mA @ 2.0 V			
Response Speed	1.2 MHz (Typical)			
Timer/Counter				
Channels	16-bit, 3-ch			
Input Frequency	10 MHz max.			
Reference Clock	Internal: 2 MHz			
General				
Bus Type	5 V PCI bus, 32-bit, 33 MHz			
Connectors	Female DB-37 x1, 20-pin box header x 2			
Power Consumption	960 mA @ +5 V			
Operating Temperature	0 °C ~ +60 °C			
Storage Temperature	-20 °C ~ +70 °C			
Humidity	5 ~ 85% RH, non-condensing			

Pin Assignments



Pin Assign- ment	To	ermi	nal N	lo.	Pin Assign- ment
DI 0	01	0	0	02	DI 1
DI 2	03	0	0	04	DI 3
DI 4	05	0	0	06	DI 5
DI 6	07	Lo	0	08	DI 7
DI 8	09	0	0	10	DI 9
DI 10	11	0	0	12	DI 11
DI 12	13	ro.	0	14	DI 13
DI 14	15	0	0	16	DI 15
GND	17	0	0	18	GND
+5V	19	0	0	20	+12V
		CC	N1		

Pin Assign- ment	To	ermir	nal N	lo.	Pin Assign- ment
DO 0	01	0	0	02	DO 1
DO 2	03	0	0	04	DO 3
DO 4	05	0	0	06	DO 5
DO 6	07	Lo	0	08	DO 7
DO 8	09	0	0	10	DO 9
DO 10	10	0	0	12	DO 11
DO 12	12	0	0	14	DO 13
DO 14	14	0	0	16	DO 15
GND	16	0	0	18	GND
+5V	18	0	0	20	+12V
		CC	N2		

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Ordering Information .

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	PIO-821L	PCI bus, 16-ch 12-bit, 45 kS/s Low gain Multi-function Board.
	PIO-821H	PCI bus, 16-ch 12-bit, 45 kS/s High gain Multi-function Board.

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